

IN THE SPECIFICATION

Please replace the paragraph beginning on page 1, line 12 with the following rewritten paragraph:

A1

Figure 1 illustrates a home phone line network in accordance with the present invention. The preferred embodiment of the network complies with the Home Phoneline Networking Alliance specification version 2.0 (HPNA 2.0). The network allows multiple computers to communicate through telephone wires typically installed in residential homes. The network comprises a control chip 100. The chip 100 further comprises a Media Independent Interface (MII) 106, a Media Access Control (MAC) 108, and a Physical Layer (PHY) 110. The chip 100 implements the HPNA 2.0 specification. The chip 100 receives a signal containing data packets through the telephone wires via a phone jack 102. There is an analog front end (AFE) 104 which processes the signal between the chip 100 and the telephone wires. The chip 100 then processes the packets received in the signal from the AFE 104, and outputs a signal to the Host MAC ~~112~~ 114 or to an Ethernet controller 112.

Please replace the paragraph beginning on page 7, line 1 with the following rewritten paragraph:

A2

Figure 4 illustrates a preferred embodiment of a Receive Data Path which strips the LARQ header to support remote wake up in accordance with the present invention. The Receive Data Path 202 comprises a PM\_RXDEC logic block 402, a PM\_RXFCON logic block 404, a PM\_RXNIB logic block 406, and a PM\_RXFCSGEN logic block 408. The registers in the Registers and MIB Counters 216 asserts a signal, strip\_LARQ, to the PM\_RXDEC 402 to indicate that the LARQ header stripping is enabled. When a frame with a LARQ header is received via data buses RM)BYTE to the PM\_RXDEC 402, the LARQ header is ~~tripped~~ stripped from the frame, via step 302. The PM\_RXDEC 402 then asserts the rm\_sfcs signal and the rm\_slarq signal to the PM\_RXFCON 404. The asserted rm\_sfcs signal indicates that the FCS in the frame is to be stripped. The asserted rm\_slarq signal indicates that the

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LARQ in the frame is to be stripped. The PM\_RXFCON 404 then strips the FCS and the LARQ, via step 304. Next, the PM\_RXFCON 404 asserts a rb\_str\_larq signal to the PM\_RXNIB 406. The asserted rb\_str\_larq signal indicates that the LARQ has been stripped from the frame. The PM\_RXNIB 406 generates the frame accordingly. The PM\_RXNIB 406 asserts an enfcs signal to the PM\_RXFCSGEN 408. The asserted enfcs signal enables FCS recalculation for the stripped frame. The PM\_RXFCSGEN 408 then recalculates the FCS for the stripped frame, via step 306. The recalculated FCS is added to the stripped frame, and this frame is sent to the Ethernet controller 112, via step 308.

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